

IFW

**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Terunao HANAOKA et al.

Application No.: 10/768,168

Docket No.: 109690.01

Filed: February 2, 2004

For: Semiconductor Device, Method of Fabricating the Same, Stack-Type Semiconductor Device, Circuit Board and Electronic Instrument

**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

James A. Oliff  
Registration No. 27,075

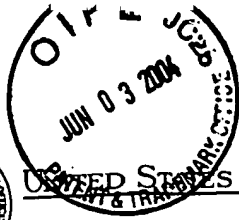
Thomas J. Pardini  
Registration No. 30,411

JAO:TJP/jos

Date: June 3, 2004

**OLIFF & BERRIDGE, PLC**  
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<p><b>DEPOSIT ACCOUNT USE AUTHORIZATION</b> Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
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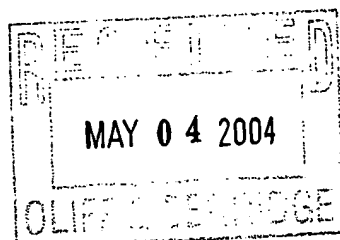
UNITED STATES DEPARTMENT OF COMMERCE  
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/768,168	02/02/2004	2811	824	109690.01	17	23	1

CONFIRMATION NO. 6030

25944  
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 P.O. BOX 19928  
 ALEXANDRIA, VA 22320



## FILING RECEIPT



\*OC000000012497725\*

Date Mailed: 05/03/2004

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## Applicant(s)

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 Kenji Wada, ~~Suwa-shi~~, JAPAN;

Fujimi-machi

## Assignment For Published Patent Application

Seiko Epson Corporation, Tokyo, JAPAN;

## Domestic Priority data as claimed by applicant

This application is a DIV of 09/870,710 06/01/2001 PAT 6,720,661

## Foreign Applications

JAPAN 2000-166104 06/02/2000  
 JAPAN 2000-338737 11/07/2000  
 JAPAN 2001-125581 04/24/2001

If Required, Foreign Filing License Granted: 04/30/2004

Projected Publication Date: 08/12/2004

Non-Publication Request: No

Early Publication Request: No

**Title**

Semiconductor device, method of fabricating the same, stack-type semiconductor device, circuit board and electronic instrument

**Preliminary Class**

257

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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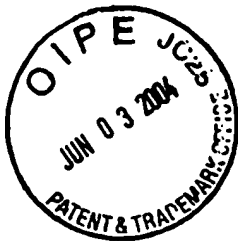
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ATTORNEYS AT LAW

**Supplemental Application Data Sheet****Application Information**

Application Type::	Regular
Subject Matter::	Utility
CD-ROM or CD-R:	None
Title::	SEMICONDUCTOR DEVICE, METHOD OF FABRICATING THE SAME, STACK-TYPE SEMICONDUCTOR DEVICE, CIRCUIT BOARD AND ELECTRONIC INSTRUMENT
Attorney Docket Number::	109690.01
Suggested Drawing Figure::	1
Total Drawing Sheets::	17
Small Entity::	No

**Applicant Information**

Applicant Authority type::	Inventor
Primary Citizenship Country::	Japan
Status::	Full Capacity
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Middle Name::	
Family Name::	HANAOKA
Name Suffix::	
City of Residence::	Fujimi-machi
State or Province of Residence::	
Country of Residence::	Japan

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Applicant Authority type::	Inventor
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Middle Name::	

Family Name:: WADA  
 Name Suffix::  
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 State or Province of Residence::  
 Country of Residence:: Japan

### Correspondence Information

Correspondence Customer Number:: 25944

<b>Domestic Priority Information</b>			
Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application is a	Divisional of	09/870,710	June 1, 2001
This Application is a	Continuation of		MM / DD / YY
<b>Foreign Priority Information</b>			
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	JP 2000-166104	June 2, 2000	Yes
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	JP2000-338737	November 7, 2000	Yes
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	JP 2001-125581	April 24, 2001	Yes
<b>Assignee Information</b>			
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Country of mailing address::		Japan	
Postal or Zip Code of mailing address::			